

AcuProx Innóvations

07082122



AX-12LA RFID Reader

The AX-12LA reader is part of AcuProx line of products for access control. It is indicated for applications that use cards and tags operating at 125 kHz. With small size is ideal for integrating turnstiles and similar equipment for access control.

Purchase Code: 500.613

Features

The AX-12LA is a reader of frills, but great reading performance, with range up to 18 cm with cards AcuProx line. Due to its size, is indicated primarily for use in clocks, turnstiles and other similar equipment in access control applications. Communication allows three interfaces in the same product: TTL RS-232, Wiegand 26 bits and ABA TK2. Moreover, it presents pinout that facilitates the connection on the board. Thus, the AX-12LA reading module is a smart and simple solution for most applications in radio frequency identification.

Technical Specifications

Power	2,8 to 5 VDC
Consumption	35 mA @ 5 VDC
Communication Interfaces	Wiegand 26 bits, Magstripe (ABA TK2) and Serial TTL RS-232
Reading distance*	Up to 12 cm, with an AcuProx Card ISO Up to 12 cm, with an AcuProx Card Up to 18 cm, with an AcuProx Card HP
Frequency of operation	LF - 125 kHz
Modulation	ASK
Protocol	ISO 11784/85
Transponder	Only reading cards and tags AcuProx
Dimensions	26 x 25 x 6 mm
Weight	6 g
Operating Temperature	-10°C to 60°C
Humidity	10% to 90% (non-condensing)
Protection degree	Not apply
Installation	Embedded in equipment

* The reading distances may vary more or less depending on the type of card used, the operating environment

Data Structure

Serial ASCII (TTL RS-232)			
Speed: 9600 k	bps, no parity, 8 data bits, 1 stop	o bit.		
STX (02 HEX)	DATA (10 CHARACTERS ASCII)	CR (OD HEX)	LF (OA HEX)	ETX (03 HEX)

Magstripe Emulation (ABA Track 2)

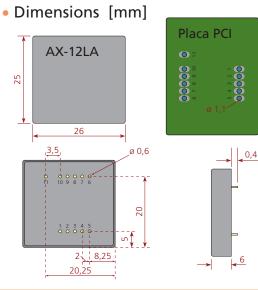
Speed: 40 IPS simulated (inches per second)

SEQ. OF 10 ZEROS	SS	DATA (14 DIGITS)	ES	LRC	SEQ. OF 10 ZEROS
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Wiegand (26 bits)

Rev. A.3

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25
Р	S	S	S	S	S	S	S	S	С	С	С	С	С	С	С	С	С	С	С	С	С	С	С	С	Ρ
	E	Ε	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	0	0	0	0	0	0	0	0	0	0	0	0	
	Bits (E) for calculating the Parity (P) Couple											[Bits (0) fo	or ca	lcula	ting	the	Parit	y (P)	Odd				



and the power source used